

Department of Electrical and Computer Engineering

Title: «*Cy-Tera: A computational resource for Cyprus* »

Jens Wiegand

DPhil in Engineering Science - University of Oxford, UK
Dipl-Ing. in Mechanical Engineering - TU-Dresden in Germany

George Tsouloupas

PhD – in the area of monitoring and performance evaluation
in High Throughput and High Performance Computing, University of Cyprus

Wednesday, 30th October 2013, 17:00 – 18:30

**Room ΠΤΕΡ Ε113, Old Campus
University of Cyprus**

Abstract:

The Computation-based Science and Technology Research Center (CaSToRC) of the Cyprus Institute has developed a Tier-1 high-performance computing (HPC) facility - Cy-Tera. CaSToRC aspires to cultivate the use of high performance computing in Cyprus and the Eastern Mediterranean region and to serve the needs for HPC and data intensive computing in various scientific fields. The seminar will introduce you to available computational resources for Cypriot scientists and how to access them. A special emphasis will be given to applications available for the computational biology and bioinformatics community.

Biography:

Jens Wiegand: Jens received his Doctor of Philosophy (DPhil) in Engineering Science from the University of Oxford in the UK, after receiving a Dipl-Ing. in Mechanical Engineering from TU-Dresden in Germany. Along his academic career, Jens was involved in research related to material behaviour at high strain rates commonly observed during Impact loading. Focus of his research was on lightweight hybrid material systems in general and composite materials in particular. A major aspect of his research was the development of numerical models to represent these materials under impact loading. Jens has held a postdoctoral appointment at the University of Oxford and was research fellow at Exeter College Oxford. Prior to joining CaSToRC at Cyl, Jens held an executive role in the establishment and development of the Impact Engineering Laboratory at the University of Oxford.

Jens joined The Cyprus Institute as Scientific Project Coordinator for CaSToRC Research and has recently moved into the role of Managing Coordinator in CaSToRC. In this function, he has responsibility for the management of CaSToRC research projects and the day to day management of the centre itself. His work is mainly related to High Performance Computing with a special focus on the Eastern Mediterranean region.

George Tsouloupas: George Tsouloupas obtained his PhD from the University of Cyprus in the area of monitoring and performance evaluation in High Throughput and High Performance Computing. He contributed to several projects related to Grid Computing and HPC. He has worked in industrial research and development on diverse subjects ranging from quality of drinking water to Electronic Health Records. George is the head of HPCc systems at the Cy-Tera HPC facility hosted at the Cyprus Institute.